



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: **TAKAYAMA, Toshio, et al.**

Group Art Unit: **2811**

Serial No.: **10/735,886**

Examiner: **Nitin PAREKH**

Filed: **December 16, 2003**

P.T.O. Confirmation No.: **5693**

For: **SEMICONDUCTOR DEVICE HAVING A MULTILAYER  
INTERCONNECTION STRUCTURE AND FABRICATION PROCESS  
THEREOF**

**PETITION FOR EXTENSION OF TIME**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Date: April 29, 2005

Sir:

Applicants petition the Commissioner for Patents to extend the time for response to the Office Action dated January 14, 2005 for one month, from April 14, 2005 to May 14, 2005.

Attached please find a check in the amount of \$120.00 to cover the cost of the extension for a large entity. In the event that any additional fees are due in connection with this paper, please charge our Deposit Account No. 01-2340.

Respectfully submitted,

05/03/2005 HALI11 00000097 10735886

01 FC:1251

120.00 DP

**ARMSTRONG, KRATZ, QUINTOS,  
HANSON & BROOKS, LLP**

Mel R. Quintos

Attorney for Applicants

Reg. No. 31,898

MRQ/lrj  
Atty. Docket No. **031325**  
Suite 1000  
1725 K Street, N.W.  
Washington, D.C. 20006  
(202) 659-2930



**23850**

PATENT TRADEMARK OFFICE